



## INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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<p>(21) International Application Number: PCT/EP95/04972</p> <p>(22) International Filing Date: 15 December 1995 (15.12.95)</p> <p>(71) Applicant (for all designated States except IT US): INTERNATIONAL BUSINESS MACHINES CORPORATION [US/US]; Old Orchard Road, Armonk, NY 10504 (US).</p> <p>(71) Applicant (for IT only): IBM SEMENTA S.P.A. [IT/IT]; Via Tolmezzo, 15, I-20132 Milano (IT).</p> <p>(72) Inventor; and</p> <p>(75) Inventor/Applicant (for US only): OGGIONI, Stefano [IT/IT]; Via Brenta, 13, I-20059 Vimercate (IT).</p> <p>(74) Agent: ZERBI, Guido, Maria; Intellectual Property Dept., IBM United Kingdom Limited, Hursley Park, Winchester, Hampshire SO21 2JN (GB).</p>		<p>(81) Designated States: JP, US, European patent (AT, BE, CH, DE, DK, ES, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).</p> <p><b>Published</b> With international search report.</p>
<p>(54) Title: ELECTRONIC PACKAGE WITH SPACER MEANS</p> <div data-bbox="524 1073 1235 1577"> </div> <p>(57) Abstract</p> <p>An electronic package, particularly a BGA, including a die device (110) attached to a substrate (120) by means of an attach material (210), commonly a glue. This glue layer (210) includes spacer means (310) providing a high thick glue layer setting a required stand-off between the die device (110) and the substrate (120). These spacer means are preferably small spheres made of glass or corundum, with a diameter comprised between 20µm and 130µm. At least three spheres are required for determining a sitting plane for the die device (110); the possible quantity of included spacer means (310), without affecting the functionality of the glue material in its performances as adhesion, heat transfer, electrical conductivity, is up to the 30 % of the total volume of the attach material (210) between the die device (110) and the substrate (120).</p>		

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## ELECTRONIC PACKAGE WITH SPACER MEANS

The present invention relates to an electronic package and particularly, but not exclusively, to an electronic package including at least one die device having a first CTE  
5 (Coefficient of Thermal Expansion) and a substrate having a second CTE, said die device being attached to said substrate by means of an intermediate layer including an attach material having a third CTE greater than the lower between said first and said second CTE.

10 The use of an attach material, such as a glue, is common in electronic packaging applications for attaching die devices or components to a substrate, particularly in Ball Grid Array (BGA) packages.

BGA packages or modules are a quite recent development  
15 in the electronic packaging industry replacing the current products as the Quad Flat Packs (QFP). The main difference is the connection system to the printed circuit board (PCB), also called second level attachment, that is made through eutectic Tin Lead alloy balls arranged in a matrix lay-out on the  
20 bottom side of a substrate, instead of metal leads placed along the peripheral edge of the plastic component body. One or more die devices are then attached on the top side of the substrate by means of a glue layer. BGA and QFP packages are described in "Circuits Assembly (USA) - Vol.6, No.3 March 1995  
25 Pag.38-40".

A problem in these electronic packages is due to the difference of the Coefficient of Thermal Expansion (CTE) of the substrate and the die device. The difference of the Coefficient of Thermal Expansion creates regions where strains  
30 are resulting from the presence of a first stiffener material that reduces the normal expendability of the second material.

This problem is particular serious in BGA packages, where these strains are acting on the balls connecting the module to the Printed Circuit Board, causing these balls connections  
35 starting to fracture for fatigue, particularly after thermal power-on/power-off cycling. All the eutectic balls included

in corresponding area of the attached die to the substrate have cracks, exposing the BGA package to reliability problems. The driver for these cracks is the different Coefficient of Thermal Expansion of the die device that is lower than the substrate. The result of this expendability mismatch is a localized stiffness of the substrate that is not allowed to expand as it would without the die presence. When this module is attached to a printed circuit mother board, that is also made with epoxy woven fiberglass laminate, the module and the board should be in an ideal situation and should expand at the very same range of CTE values: unfortunately in the module area underneath the die device the substrate material behaves with a different CTE. All the balls connections within this area are then exposed to a strain driven by the unequal material expansion factors where the balls two ends are attached to. All the balls placed underneath the die attach area ended up with their fracture for fatigue with a resulting reliability exposure for the user or driving the non utilization of those module Input/Outputs.

The solution known in the art for relieving the stiffness driven by the attached die device to the substrate is that of using a thick layer of solder mask to create a cushion effect between the die device rigidity and the substrate expendability. This solution involves the use of a special solder mask (dry films) in order to increase the thickness of the protective material layer on the module substrate top side; the die device is then attached to a layer of this insulating material. This solution proved to work at a relatively low cost; the working thicknesses are achievable with some materials and related processes only, but once they are implemented no further special precautions/process steps are required.

A drawback with this prior art is the lower capability of thermal dissipation of the package, because heat can be dissipated through the module substrate core only. In addition, the insulating material layer thickness reduces the number of the possible applications.

In order to avoid the previously mentioned heat transfer problems, instead of having a thick layer of insulating extra material, a different solution would be to increase the thickness of the attach material that has a compatible CTE  
5 with the substrate material; this material may have elastic properties, being also thermally conductive. The difficulty of this approach is due to the fact that the attach material has a medium low viscosity, so that the die can easily float above the glue dot or sink in it causing a scrap part.

10 A solution would require the usage of Computer Numeric Controlled (CNC) machines capable of placing the die on a theoretical plane within the glue dot; unfortunately either the substrate and the die device thicknesses have wide tolerances making the resulting glue thickness very  
15 theoretical. The same result would be achieved with a cheaper machine working with a positive mechanical stop, the variability of the two thicknesses will affect the final results. Either equipment supported solutions do not guarantee that the planned stand off is not affected by the  
20 polymerization process of the glue resin. In addition, they involve a high cost versus the resulting low process control and the high impact on the product price, well above the target for this low cost electronic package.

The above drawbacks of the prior art are overcome by the  
25 invention as claimed. Accordingly, the present invention provides an electronic package as set out above which is characterized in that said intermediate layer further includes spacer means for spacing said die device from said substrate.

These spacers means are not collapsible inclusion  
30 within the attach material, stopping the die device sinking in it and automatically setting the required stand-off of die attach material between the die device and the substrate, providing a high thick attach material layer.

This attach material layer provides a cushion effect  
35 between the die device and the substrate; the present invention allows then obtaining strain relieved packages in the die attach area, with the result of avoiding fatigue

failures.

It should be noted that the proposed invention is fully compatible with the existing materials and does not affect their physical and chemical properties. It is a low cost and  
5 of an extremely easy implementation. In addition, this technique guarantee a silicon chip planarity with better yield in automatic bonding operation reducing the silicon tilt yield detractor.

The solution according to the present invention may be  
10 applied both when the substrate is more rigid that the device and on the contrary when the substrate material has a CTE lower that the CTE of the die device material.

Typically, said first CTE is lower than said second CTE. The substrate materials commonly used in fact are rigid,  
15 having a high CTE, such as plastic material, fiberglass laminate, epoxy reinforced woven fiberglass laminate, ceramic, polyimide, alumina. On the contrary, the die device is typically realized with a material having a lower CTE, for example Silicon, Germanium or Gallium Arsenide.

20 In a particular embodiment of the present invention, said attach material is a glue, such as a thermoplastic glue, a termoset glue or a mix of thermoplastic and termoset materials; typically, the glue is an epoxy glue, generally loaded with Silver particles for a better heat dissipation.

25 Advantageously, said spacer means have a fourth CTE not greater than said third CTE.

This feature reduces the risk of any detachment of the die device from the substrate, due to the expendability of the spacers.

30 Particularly, said spacer means are made of glass or corundum.

The glass is useful in the case the CTE has to be driven by the compatibility with the composite laminate fiberglass material; the corundum is preferred in the case the CTE has to  
35 be the closest possible to the Silicon or Alumina CTE.

Advantageously, the volume of said spacer means is not more that the 30% of the volume of said intermediate layer.

This quantity of included spacer means does not affect the functionality of the attach material in its performances as adhesion, heat transfer and electrical conductivity.

Different types of spacer means may be used according to the present invention, such as rods, cubes; in an advantageous embodiment, said spacer means include at least three spheres.

These spheres may be easily mixed to the attach material and does not have any problem in the dispensing of the same attach material onto the substrate; in addition, they have a very low cost being commonly used for sandblasting operations. The number of three spheres per dispensed attach material spot is in addition useful for determining a sitting plane for the die device.

These balls can be mixed or added to the die attach glue with a predetermined diameter based on the targeted glue thickness. Typically, said spheres have a diameter comprised between 20 $\mu$ m and 130 $\mu$ m.

In a particular advantageous embodiment of the present invention, said electronic package is a BGA.

The application of this solution to a BGA package allows obtaining strain relieved BGA in the die attach area, with the result of avoiding fatigue failures at second level connection joints. All the balls placed underneath the die attach area are then available, increasing the Input/Output capability of the electronic package and allowing production of smaller BGA.

The present invention also provides a method of manufacturing an electronic package including a die device having a first CTE (Coefficient of Thermal Expansion) and a substrate having a second CTE, said method including the steps of dispensing an attach material onto said substrate, said attach material having a third CTE greater than the lower between said first and said second CTE, attaching said die device to said substrate by means of said attach material, characterized by the step of mixing spacer means into said attach material for spacing said die device from said substrate.

The packaging method according to the present invention

does not require any additional step of forming a dry film onto the substrate; in addition, it is fully compatible with the current processes and related equipment used in the industry.

5           Advantageously, said step of mixing is performed before said step of dispensing. This embodiment it is low cost and does not require any additional equipment for placing the spacer means in the attach material after dispensing.

10           Various embodiments of the invention will now be described in detail by way of examples, with reference to accompanying figures, where:

Fig. 1 is an electronic package according to the prior art;

15           Fig. 2 depicts an electronic package according to an embodiment of the present invention;

Fig. 3 shows the high thick glue layer used in an embodiment of the present invention.

20           With reference now to the figures and in particular with reference to Fig.1, a cross-sectional view of an electronic package according to the prior art is shown. The figure depicts in particular a BGA 100 including a die device 110 attached to a substrate 120. The substrate 120 is provided on its bottom side with a plurality of connecting balls or bumps 142, 144 arranged in a matrix lay-out; the connecting balls 142, 144 are typically eutectic solder, such as Tin Lead alloy. These balls 142, 144 are used to connect the BGA package to a Printed Circuit Board (not shown). Different types of BGA are available, such as Plastic Ball Grid Array (PBGA), Ceramic Ball Grid Array (CBGA) and Tape Ball Grid Array (TBGA), the primary difference being the type of substrate material. This substrate 120 is generally fairly thin, in the order of 0.3 - 0.7 mm; this values increase the problem involved by the difference in the CTE of the die device 110 and the substrate 120.

35           A thick layer of solder mask (dry film) 150 is provided in order to increase the thickness of the protective material layer on the module substrate 120 top side, creating a cushion



effect between the die device 110 rigidity and the substrate 120 expansivity.

The die device 110 is then attached to a layer of this insulating material 150 by means of the glue 130.

5       The device 110 is wired to the electrical circuit on the substrate 120 by means of wires 160, through a thermo-sonic wire bonding operation and the assembly is then molded with a plastic resin 170.

10       With reference now to Fig.2, a cross-sectional view of an electronic package according to an embodiment of the present invention is shown. The figure depicts a BGA 200, wherein the die device 110 is attached to the substrate 120.

15       The substrate 120 is realized with different materials, such as plastic material, fiberglass laminate, epoxy reinforced woven fiberglass laminate, ceramic, polyimide, alumina. The substrate materials commonly used are rigid, having a high Coefficient of Thermal Expansion, with a value typically between 14 ppm and 25 ppm.

20       On the contrary, the die device 110, such as a chip or an active device, is typically realized with a material having a lower CTE; typically, such die 110 is made of Silicon, Germanium or Gallium Arsenide, with a CTE usually comprised between 2.5 ppm to 5 ppm.

25       In the depicted embodiment of the present invention, the die device 110 is attached to the substrate 120 by means of a high thick attach material layer 210, providing a predetermined mechanical stand-off between the die device 110 and the substrate 120. The attach material has a low viscosity, commonly comprised between 500 and 50.000 cps at 30   25°C; typically, the attach material is a glue, such as a thermoplastic glue, a termoset glue or a mix of thermoplastic and termoset materials; preferably, the glue is an epoxy glue, generally loaded with Silver particles for a better heat dissipation. The attach material 210 has a CTE that is 35   greater than the CTE of the die device, thereby providing a cushion effect between the die device 110 rigidity and the substrate 120 expansivity. The present invention allows then

obtaining strain relieved BGA packages in the die attach area, with the result of avoiding fatigue failures at second level connection joints. All the balls 142 and 144, including the balls 144 placed underneath the die attach area, are then  
5 available, increasing the Input/Output capability of the electronic package and allowing production of smaller BGA. Those skilled in the art will appreciate that this solution is applicable even if the CTE of the die device is greater than the CTE of the substrate.

10 Fig. 3 shows in detail the high thick attach material layer used in an embodiment of the present invention. The controlled stand-off of the attach material 210 between the die device 110 and the substrate 120 is obtained by means of a plurality of spacer means 310. These spacers are not  
15 collapsible inclusion within the glue 210, stopping the die device sinking in the glue and automatically setting the required stand-off of die attach material.

Different types of spacers 310 may be used according to the present invention, such a rods, cubes; in an advantageous  
20 embodiment, these spacers 310 are small spheres. These spheres may be easily mixed to the attach material and does not have any problem in the dispensing of this glue onto the substrate; in addition, they have a very low cost being commonly used for sandblasting operations and they are  
25 available commercially in several diameters. These balls can be mixed or added to the die attach glue with a predetermined diameter based on the targeted glue thickness. Tested spheres diameters were in the range from 30  $\mu\text{m}$  to 130  $\mu\text{m}$  using commercially available die attach glues.

30 To determine a sitting plane for the die 110, a minimum number of spacers 310 are necessary, that is two rods or three cubes or spheres. In a preferred embodiment of the present invention, then the mixing operation has to statistically guarantee that at least three spheres are present per  
35 dispensed glue spot.

The possible quantity of included spheres, without affecting the functionality of the glue material in its

performances as adhesion, heat transfer, electrical conductivity, goes from three to a very high number up to representing the 30% of the total volume of the attach material (including the spacer means) between the silicon device and the die pad on the substrate.

The spacers 310 may be realized with different materials. Advantageously, the spacers 310 have a CTE not greater than the CTE of the glue material; this embodiment of the invention reduces the risk of any detachment of the die device from the substrate, due to the expansivity of the spacers. Preferably, the spacers 310 are made of glass ( $\text{SiO}_2$ ) in the case the CTE has to be driven by the compatibility with the composite laminate fiberglass material or corundum ( $\text{Al}_2\text{O}_3$ ) in the case the CTE has to be the closest possible to the Silicon or Alumina CTE.

The proposed invention is fully compatible with the existing materials and does not affect their physical and chemical properties. It is a low cost and of an extremely easy implementation. In addition, this technique guarantee a silicon chip planarity with better yield in automatic bonding operation reducing the silicon tilt yield detractor.

The packaging method involved by the present invention is fully compatible with the current processes and related equipment used in the industry.

BGA modules are commonly manufactured starting from the substrate 120, such as an epoxy reinforced woven fiberglass laminate core structure.

The glue 210, typically epoxy glue loaded with Silver particles is dispensed onto the substrate 120 top side, typically in dots or according to a cross form. The spacers 310 are mixed to the glue 210, either before or after dispensing. In an advantageous embodiment of the present invention, these spacers 310 are mixed to the glue 210 before the step of dispensing the glue 210 onto the substrate 120; this embodiment it is low cost and does not require any additional equipment for placing the spacers 310 in the glue 210.

These spacers 310 are not collapsible inclusion within the glue 310, so that the die device 110, placed either manually or by a machine, is a flat surface that will soon meet the balls in the glue, stopping its sinking in the resin  
5 and automatically setting the required die attach material stand-off.

The device 110 is then wired to electrical circuit on the substrate and molded with a plastic resin as in the prior art process.

10 It should be noted that the process according to the present invention does not require any additional step of forming a dry film onto the substrate.

CLAIMS

1. An electronic package (200) including at least one die device (110) having a first CTE (Coefficient of Thermal Expansion) and a substrate (120) having a second CTE, said die device (110) being attached to said substrate (120) by means of an intermediate layer (210) including an attach material having a third CTE greater than the lower between said first and said second CTE,  
5  
10 characterized in that  
said intermediate layer (210) further includes spacer means (310) for spacing said die device (110) from said substrate (120).
2. The electronic package (200) according to Claim 1, wherein  
15 said first CTE is lower than said second CTE.
3. The electronic package (200) according to Claim 1 or 2, wherein said attach material (210) is a glue.
4. The electronic package (200) according to any Claim from 1 to 3, wherein said spacer means (310) have a fourth CTE not  
20 greater than said third CTE.
5. The electronic package (200) according to any Claim from 1 to 4, wherein said spacer means (310) are made of glass or corundum.
6. The electronic package (200) according to any Claim from 1  
25 to 5, wherein the volume of said spacer means (310) is not more than the 30% of the volume of said intermediate layer (210).
7. The electronic package (200) according to any Claim from 1 to 6, wherein said spacer means include at least three spheres  
30 (310).

8. The electronic package (200) according to Claim 7, wherein said spheres (310) have a diameter comprised between 20 $\mu$ m and 130 $\mu$ m.

9. The electronic package (200) according to any Claim from 1  
5 to 8, wherein said electronic package is a BGA.

10. A method of manufacturing an electronic package (200) including a die device (110) having a first CTE (Coefficient of Thermal Expansion) and a substrate (120) having a second CTE, said method including the steps of:

10       dispensing an attach material (210) onto said substrate (120), said attach material (210) having a third CTE greater than the lower between said first and said second CTE,

          attaching said die device (110) to said substrate (120) by means of said attach material (210),

15       characterized by the step of

          mixing spacer means (310) into said attach material (210) for spacing said die device (110) from said substrate (120).

11. The method according to Claim 10, wherein said step of mixing is performed before said step of dispensing.

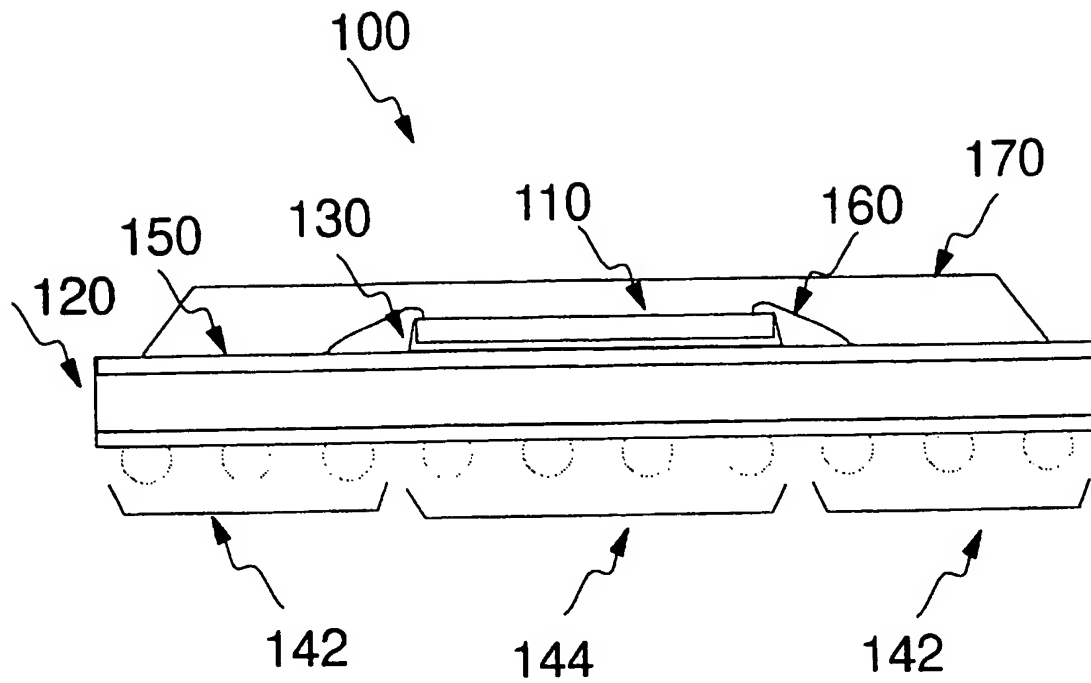


Fig. 1

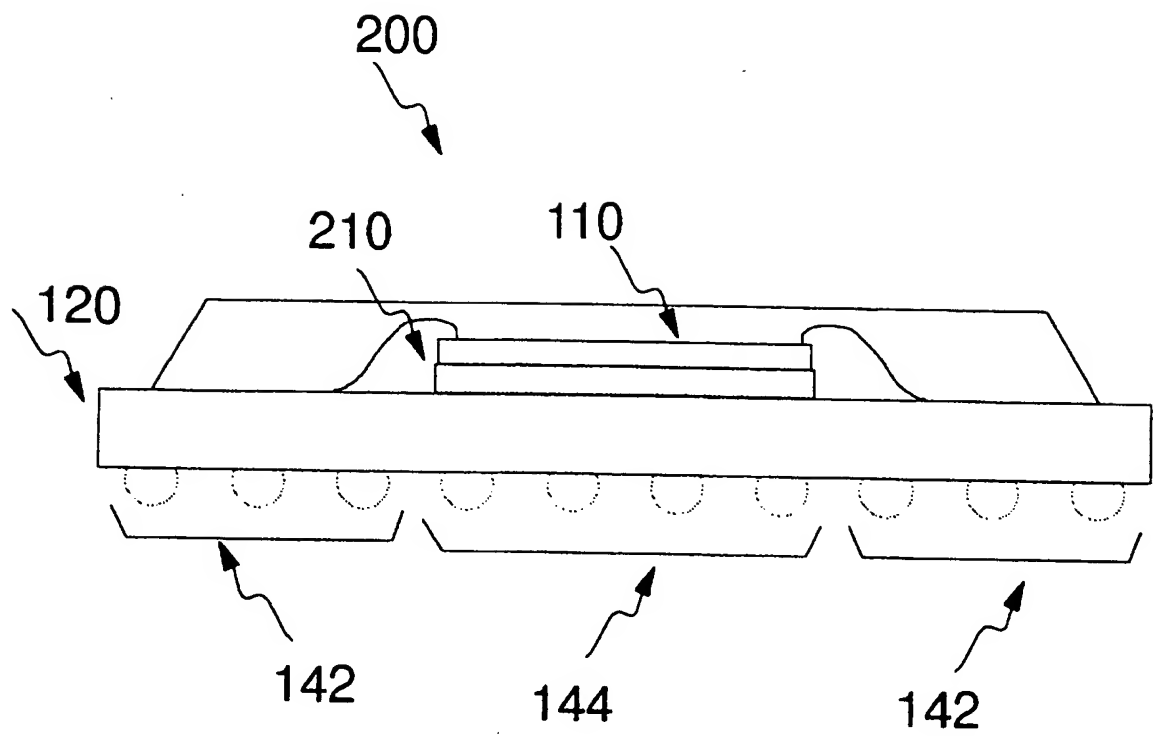


Fig. 2



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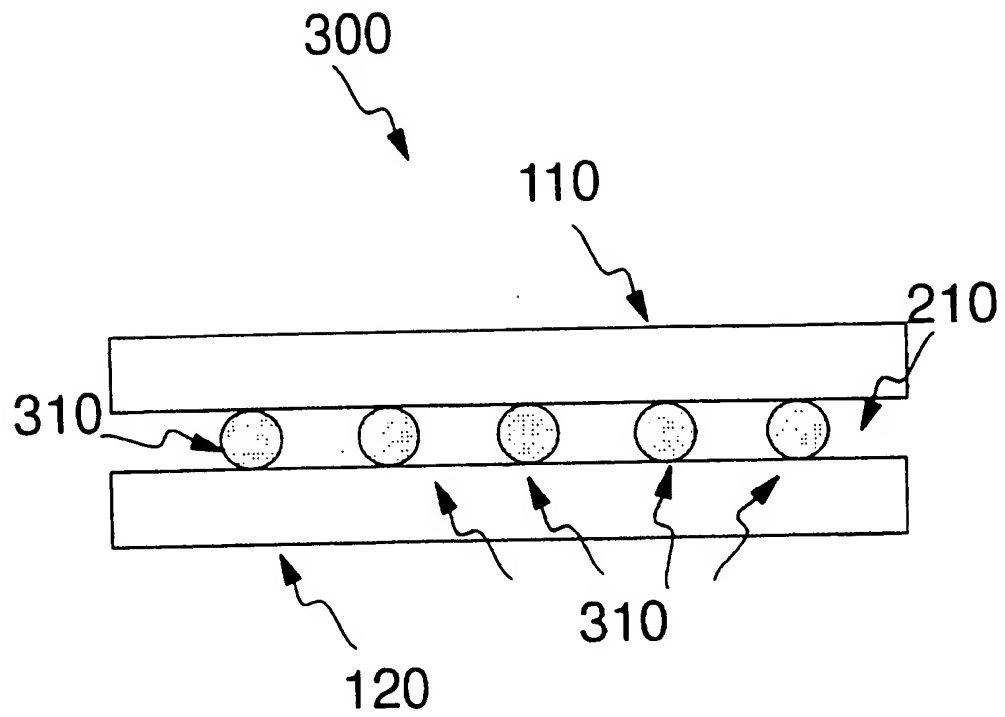


Fig. 3

# INTERNATIONAL SEARCH REPORT

International Application No

PCT/EP 95/04972

## A. CLASSIFICATION OF SUBJECT MATTER

IPC 6 H01L21/58

According to International Patent Classification (IPC) or to both national classification and IPC:

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 6 H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US,A,4 545 840 (NEWMAN ROBERT ET AL) 8 October 1985	1
A	see column 5, line 23 - line 28; claims 1,6,9,10	3,5,7,8, 10
Y	--- PATENT ABSTRACTS OF JAPAN vol. 011, no. 007 (E-469), 9 January 1987 & JP,A,61 182215 (TOSHIBA CORP), 14 August 1986, see abstract	1
A	--- US,A,5 467 252 (NOMI VICTOR ET AL) 14 November 1995 see column 5, line 6 - line 35; figure 3 --- -/--	1,3,9

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

### \* Special categories of cited documents :

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Date of the actual completion of the international search

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30. 08. 96

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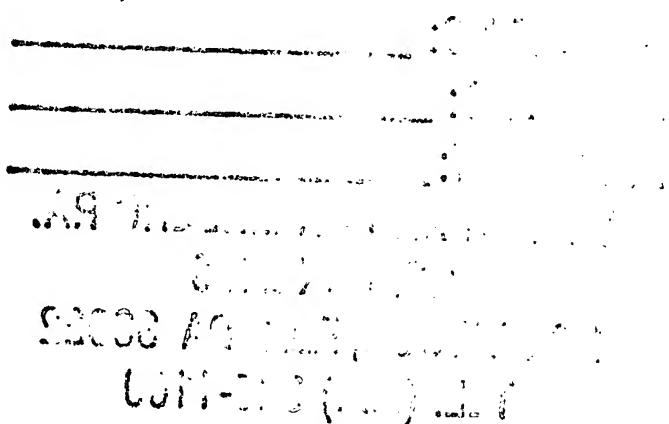
De Raeve, R

# INTERNATIONAL SEARCH REPORT

International Application No.

PCT/EP 95/04972

## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>"CHIP-HEATSINK ATTACH USING CONTOURED ADHESIVE WITH GLASS STAND-OFFS"</p> <p>1 August 1991 , IBM TECHNICAL DISCLOSURE BULLETIN, VOL. 34, NR. 3, PAGE(S) 161 - 162 XP000210487</p> <p style="text-align: center;">-----</p> <div style="text-align: center;">  </div>	

information on patent family members

PCT/EP 95/04972

US-A-4545840	08-10-85	NONE
US-A-5467252	14-11-95	NONE

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